

IN THE CLAIMS:

Please amend claims 1, 5, 7-11, 13, 14, 16, 18-25, 27, 28, 30, 47-49 and 54 as indicated in the following.

Please cancel claims 55-62 without prejudice as indicated in the following.

Claims Listing:

1. (Currently Amended) A method comprising ~~the steps of~~:
sending a first encrypted routine of a software driver to a peripheral device, wherein the software driver is to interface with the peripheral device;
decrypting, at the peripheral device, the first encrypted routine to generate a plaintext routine; and
providing the plaintext routine to the software driver.
2. (Original) The method of claim 1, wherein the first encrypted routine is an encrypted version of an encryption routine.
3. (Original) The method of claim 1, wherein the first encrypted routine is an encrypted version of a decryption routine.
4. (Original) The method of claim 1, wherein the peripheral device is a graphics chip.
5. (Currently Amended) The method of claim 4, wherein ~~the step of~~ decrypting is performed by a 3D pipe of the graphics chip.
6. (Currently Amended) The method of claim 5, wherein ~~the step of~~ decrypting is performed by a IDCT component of the graphics chip.
7. (Currently Amended) The method of claim 5, wherein ~~the step of~~ decrypting is performed by dedicated encryption hardware of the graphics chip.

8. (Currently Amended) The method of claim 1, further including ~~the step of sending a~~ decryption code to the peripheral device, where the decryption code is to be used by the peripheral device to decrypt the first encrypted routine.

9. (Currently Amended) The method of claim 1, further including ~~the step of removing~~ the plaintext routine from the software driver.

10. (Currently Amended) The method of claim 1, further including ~~the steps of:~~ encrypting, at the peripheral device, the plaintext routine to generate a second encrypted routine, where the second encrypted routine is a version of the first encrypted routine; and providing the second encrypted routine to the software driver.

11. (Currently Amended) The method of claim 10, further including ~~the step of sending~~ an encryption code to the peripheral device, where the encryption code is to be used by the peripheral device to encrypt the plaintext routine.

12. (Original) The method of claim 10, wherein the second encrypted routine is a modified version of the first encrypted routine.

13. (Currently Amended) The method of claim 1, further including ~~the step of selecting~~ the first encrypted routine from a plurality of different encrypted routines, wherein the plurality of different encrypted routines are functionally equivalent.

14. (Currently Amended) The method of claim 1, wherein ~~the step of decrypting~~ includes using a map as a decryption key.

15. (Original) The method of claim 14, wherein the map includes a texture map.

16. (Currently Amended) The method of claim 1, wherein ~~the step of providing~~ includes storing the plaintext routine in a location in memory accessible by the software driver, and where the location in memory is known to the software driver.

17. (Original) A method comprising:

sending a first encrypted routine of a software driver to a graphics chip, wherein the software driver is to interface with the graphics chip, and where the first encrypted routine is an encrypted version of an encryption routine;
decrypting, at the graphics chip, the first encrypted routine to generate a plaintext routine, wherein the plaintext routine is a version of the encryption routine; and
storing the plaintext routine in memory in a location known to the software driver.

18. (Currently Amended) The method of claim 17, further including ~~the step of sending a~~ decryption code to the graphics chip, where the decryption code is to be used by the graphics chip to decrypt the first encrypted routine.

19. (Currently Amended) The method of claim 17, wherein ~~the step of decrypting is~~ performed by a 3D pipe of the graphics chip.

20. (Currently Amended) The method of claim 17, wherein ~~the step of decrypting is~~ performed by a IDCT component of the graphics chip.

21. (Currently Amended) The method of claim 17, wherein ~~the step of decrypting is~~ performed by dedicated encryption hardware of the graphics chip.

22. (Currently Amended) The method as in claim 17, wherein ~~the step of decrypting is~~ performed through a series of components coupled within the graphics chip.

23. (Currently Amended) The method of claim 17, further including ~~the step of removing~~ the plaintext routine.

24. (Currently Amended) The method of claim 17, further including ~~the steps of:~~
encrypting, at the graphics chip, the plaintext routine to generate a second encrypted routine; and
storing the second encrypted routine in memory in a location known to the software driver.

25. (Currently Amended) The method of claim 24, further including ~~the step of sending~~ [[a]]an encryption code to the graphics chip, where the encryption code is to be used by the graphics chip to encrypt the plaintext routine.

26. (Original) The method of claim 24, wherein the second encrypted routine is a modified version of the first encrypted routine.

27. (Currently Amended) The method of claim 17, further including ~~the step of selecting~~ the first encrypted routine from a plurality of different encrypted routines, wherein the plurality of different encrypted routines are functionally equivalent.

28. (Currently Amended) The method of claim 17, wherein ~~the step of decrypting~~ includes using a map as a decryption key.

29. (Original) The method of claim 28, wherein the map includes a texture map.

30. (Currently Amended) The method of claim 17, wherein ~~the step of providing~~ includes storing the plaintext routine in a location in memory accessible by the software driver, and where the location in memory is known to the software driver.

31. (Original) A system comprising:

a processor;

memory operably coupled to said processor;

a peripheral device, said peripheral device to decrypt a first encrypted routine and generate a plaintext routine;

a software driver, wherein said software driver is to interface with said peripheral device, said software driver including a program of instructions capable of being stored in said memory and executed by said processor, said program of instructions to manipulate said processor to:

send the first encrypted routine of said software driver to said peripheral device;

and

execute the plaintext routine.

32. (Original) The system of claim 31, wherein the peripheral device includes a hardware component to encrypt the plaintext routine to generate a second encrypted routine, and where the second encrypted routine is a modified version of the first encrypted routine.

33. (Original) The system of claim 31, wherein the peripheral device includes a hardware component to decrypt the first encrypted routine to generate the plaintext routine.

34. (Original) The system of claim 33, wherein the peripheral device is a graphics chip.

35. (Original) The system of claim 33, wherein the hardware component is a 3D pipe.

36. (Original) The system of claim 33, wherein the hardware component is a IDCT component.

37. (Original) The system of claim 33, wherein the hardware component is a dedicated hardware component.

38. (Original) The system of claim 31, wherein said program of instructions further include instructions to manipulate said processor to send a decryption code to said peripheral device, where the decryption code is to be used by said peripheral device to decrypt the first encrypted routine.

39. (Original) The system of claim 31, wherein said program of instructions further include instructions to manipulate said processor to send an encryption code to said peripheral device, where the encryption code is to be used by said peripheral device to encrypt the plaintext routine.

40. (Original) A system comprising:

a first interface to receive a first encrypted routine of a software driver;

a first hardware component to decrypt the first encrypted routine received by said interface and generate a plaintext routine; and

a second interface to output the plaintext routine for use by said software driver.

41. (Original) The system as in claim 40, wherein said first interface and said second interface are implemented using a same interface.

42. (Original) The system of claim 40, further including a second hardware component to:

encrypt the plaintext routine to generate a second encrypted routine, wherein the second encrypted routine is a modified version of the first encrypted routine; and
provide the second encrypted routine to said interface.

43. (Original) The system of claim 42, wherein the first hardware component and the second component are implemented using a same hardware component.

44. (Original) The system of claim 40, wherein the hardware component is a 3D pipe.

45. (Original) The system of claim 40, wherein the hardware component is a IDCT component.

46. (Original) The system of claim 40, wherein the hardware component is a dedicated hardware component.

47. (Currently Amended) A method comprising ~~the steps of:~~
sending a first encrypted routine of a first software driver to a peripheral device, wherein
the software driver is to interface with the peripheral device;
decrypting, at the peripheral device, the first encrypted routine to generate a plaintext
routine; and
providing the plaintext routine to a second software driver.

48. (Currently Amended) The method of claim 47, wherein the second software driver
~~includes a lower level of protection~~ is less protected from unauthorized access than the first
software driver.

49. (Currently Amended) A method comprising ~~the steps of:~~

sending a first encrypted data associated with an application to a peripheral device,
wherein the application is to interface with the peripheral device;
decrypting, at the peripheral device, the first encrypted sensitive data to generate a
plaintext data; and
providing the plaintext data to the application.

50. (Original) The method of claim 49, wherein the first encrypted data includes an encrypted software routine.

51. (Original) The method of claim 49, wherein the first encrypted data includes an encrypted version of one of: a private encryption key, a private decryption key, a chip ID, and a device ID.

52. (Original) The method of claim 49, wherein the application includes a software driver.

53. (Original) The method of claim 49, wherein the peripheral device includes a graphics chip.

54. (Currently Amended) The method of claim 49, further including ~~the step of~~ encrypting, at the peripheral device, the plaintext data to generate a second encrypted data, wherein the second encrypted data is a version of the first encrypted data.

55. – 62. (Canceled)